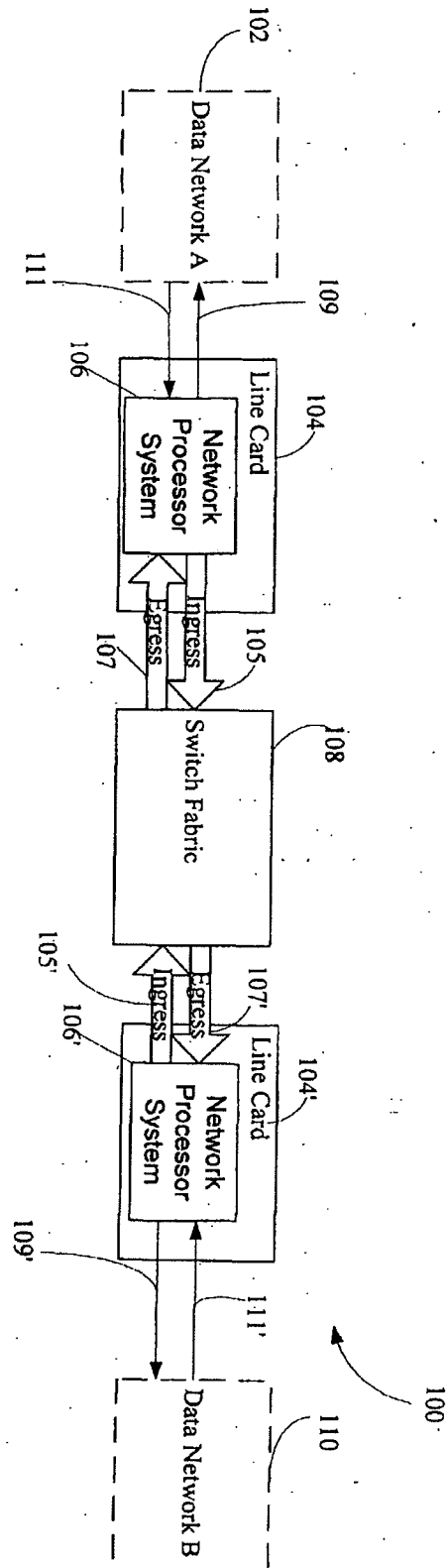


FIG. 1



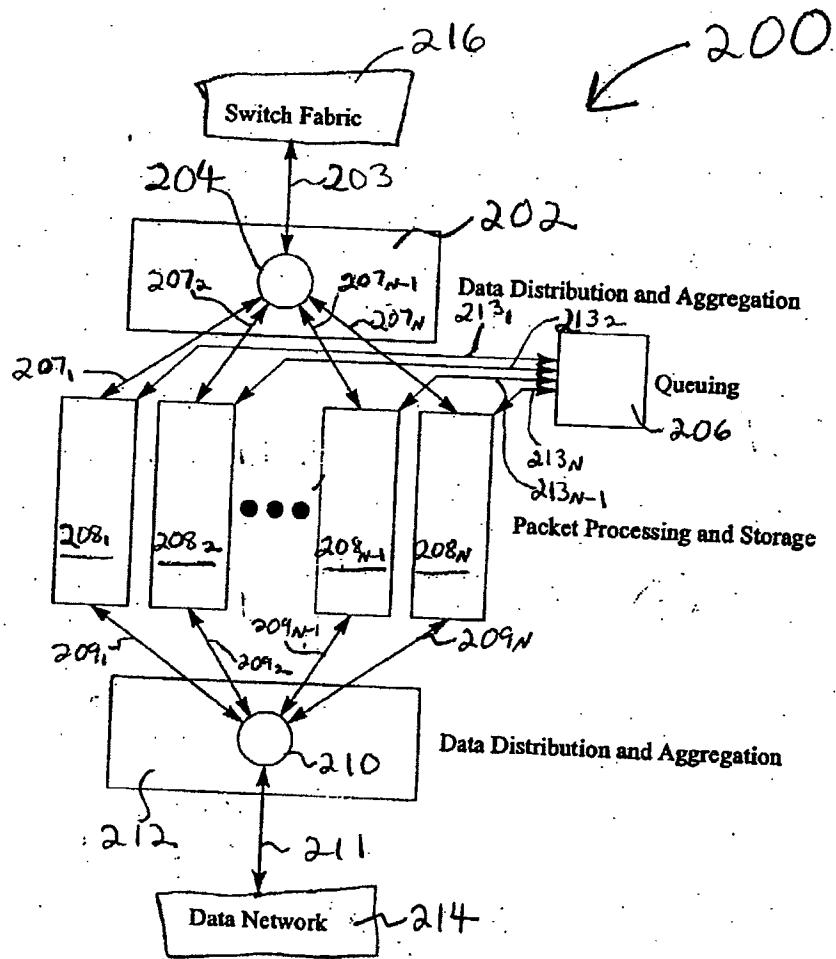


FIG. 2

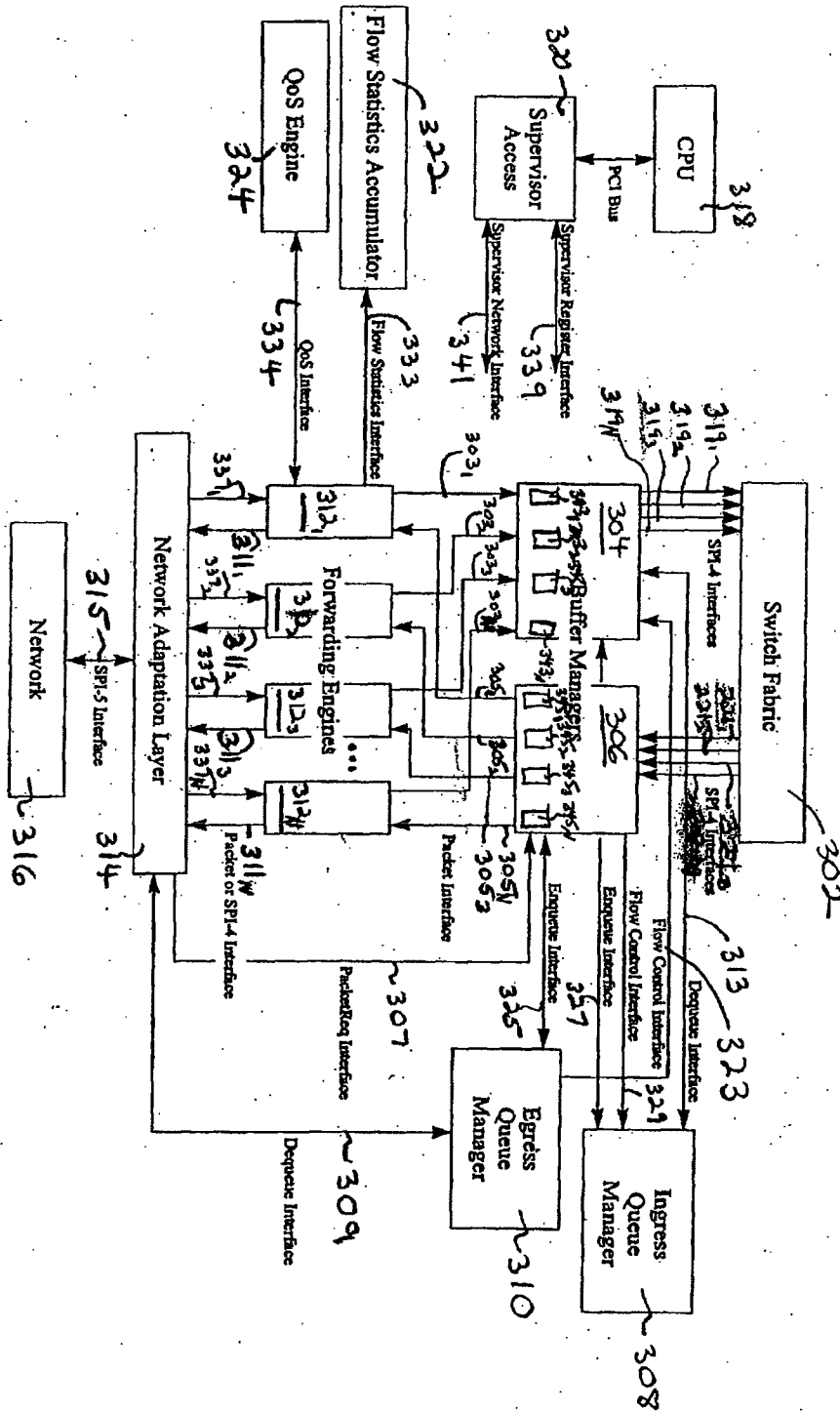


FIG. 3

300

FIG. 4A

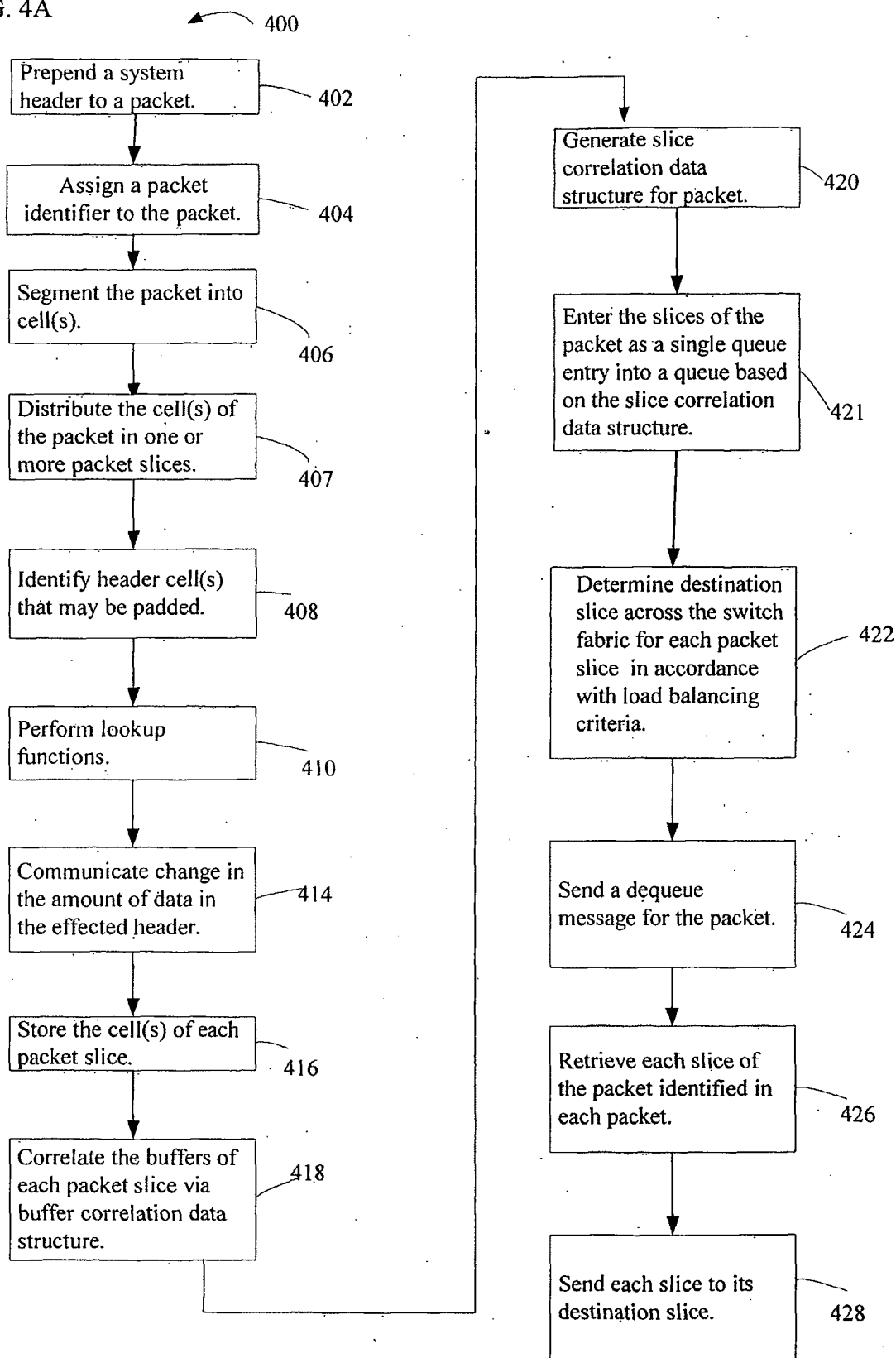


FIG. 4B

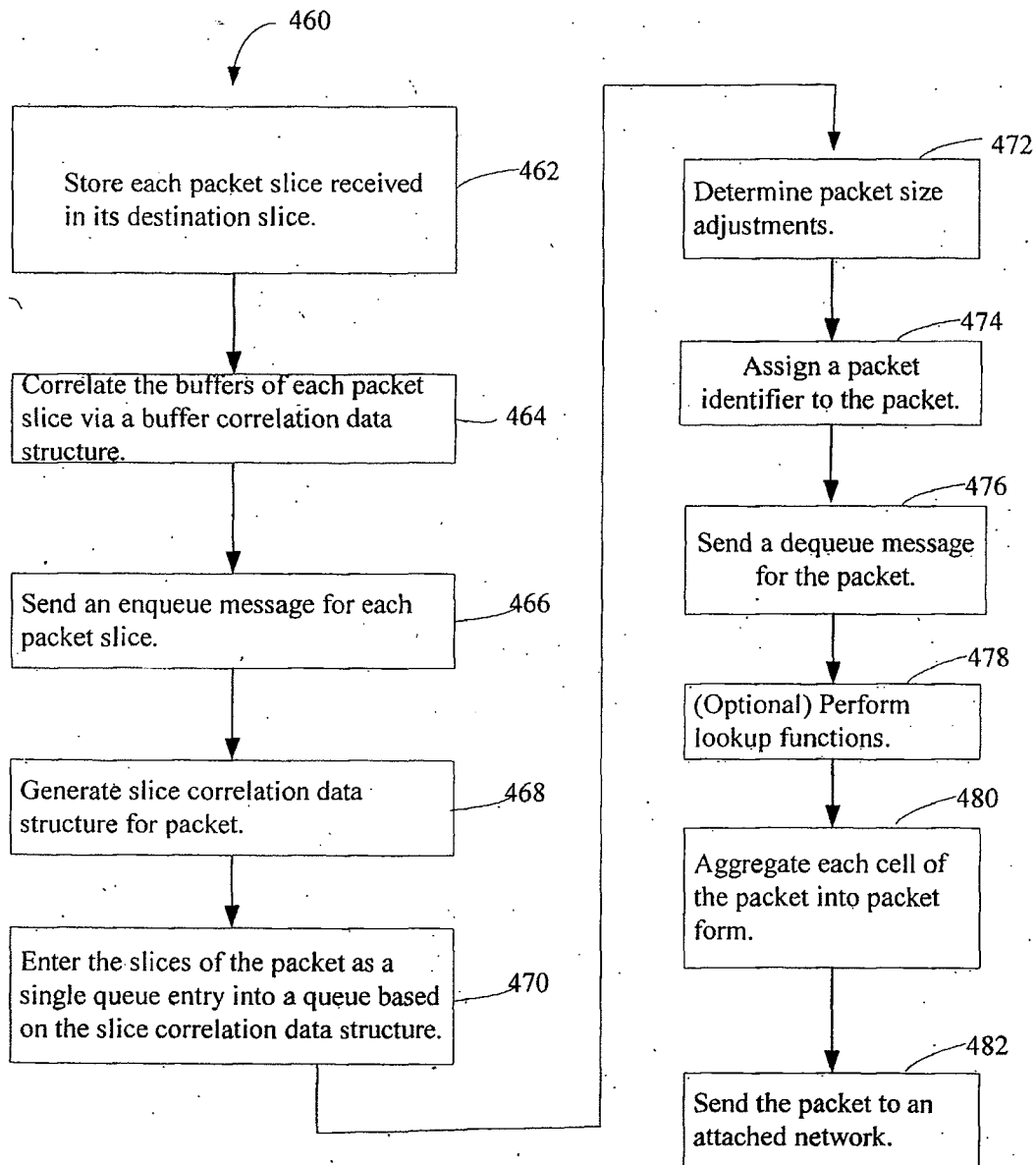


FIG. 5

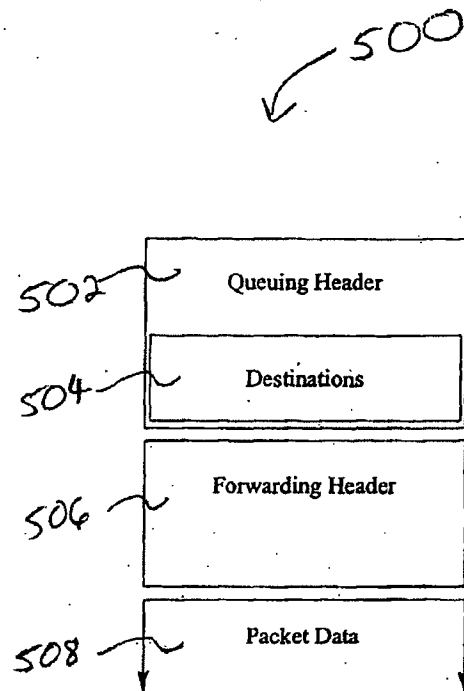
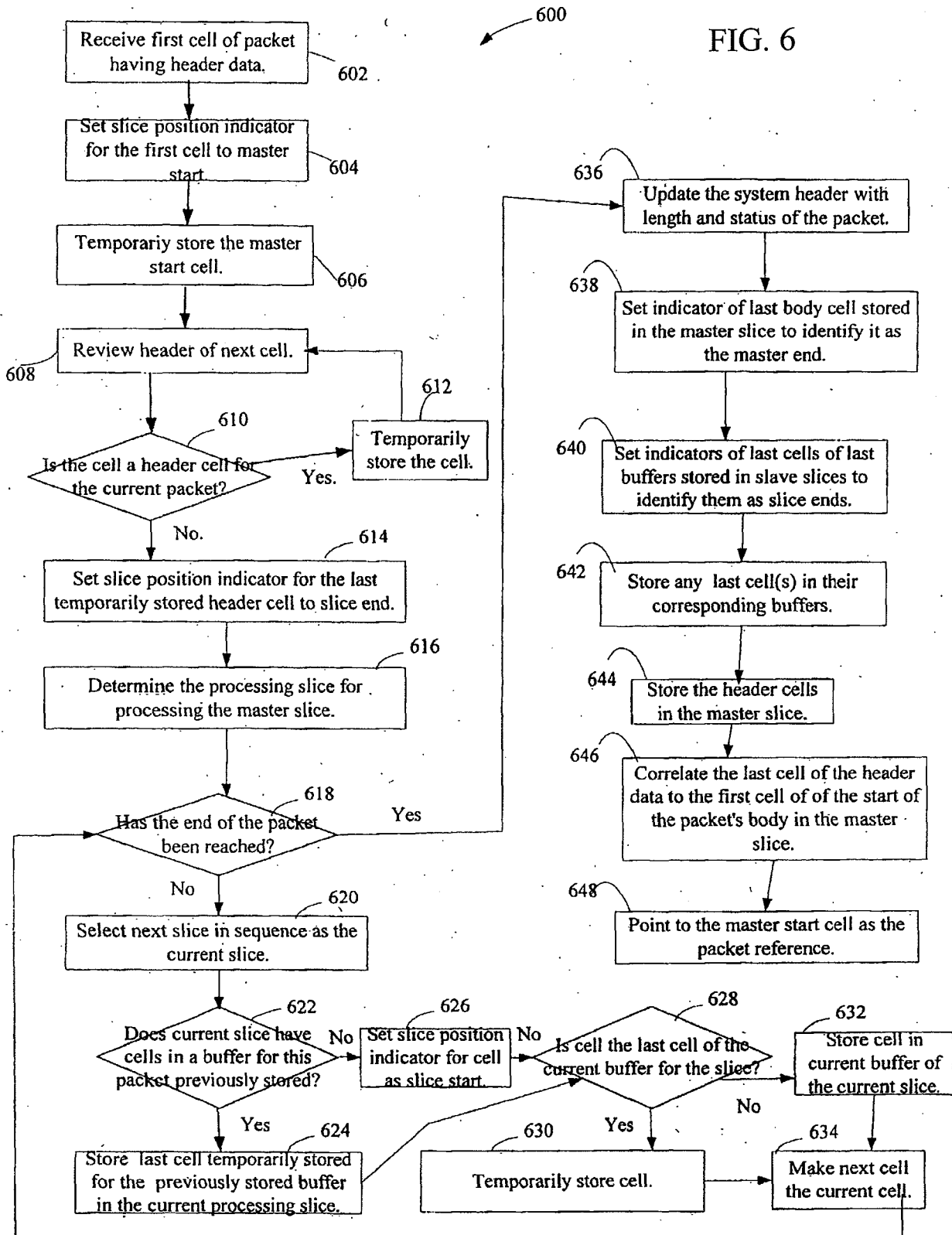


FIG. 6



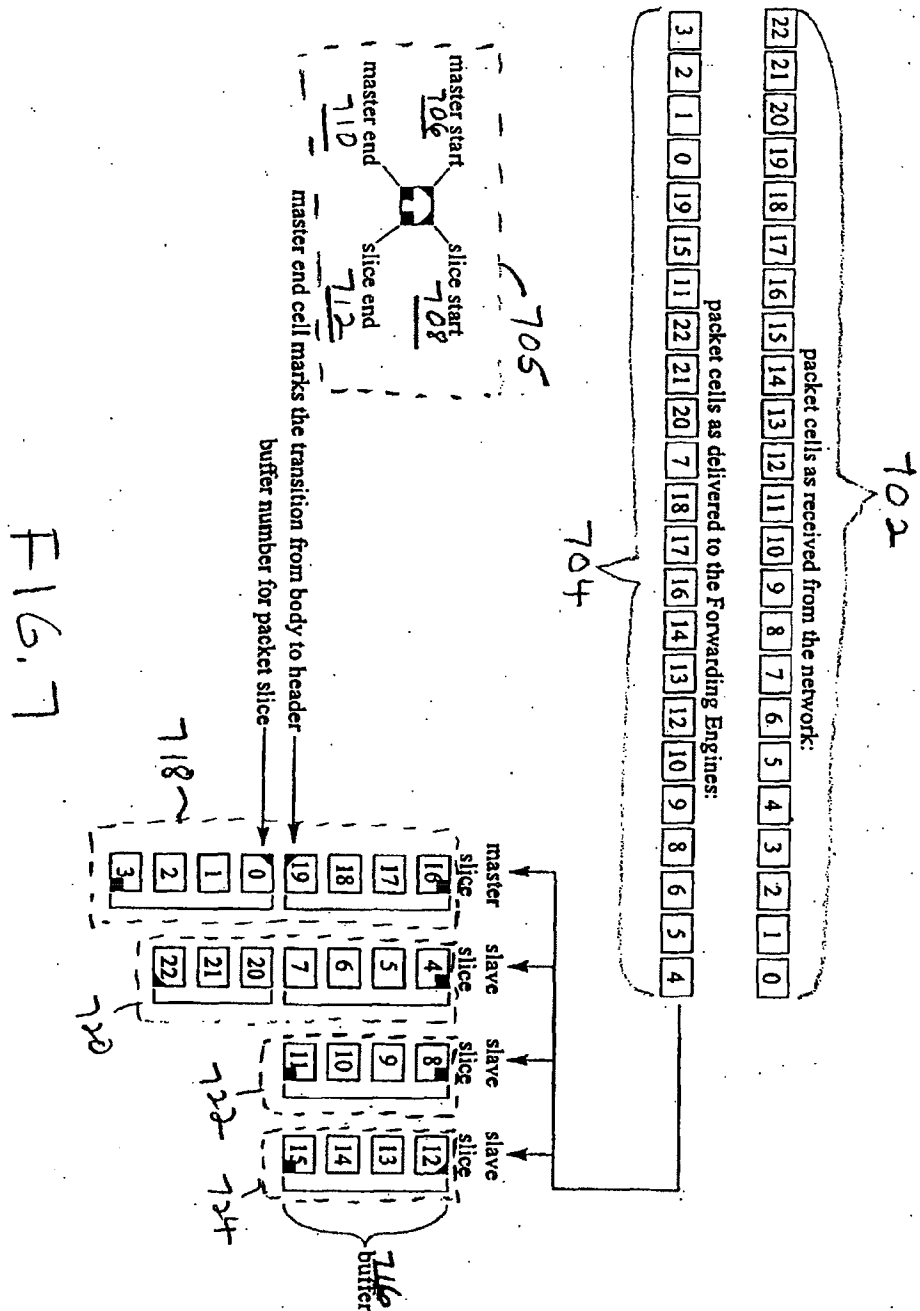
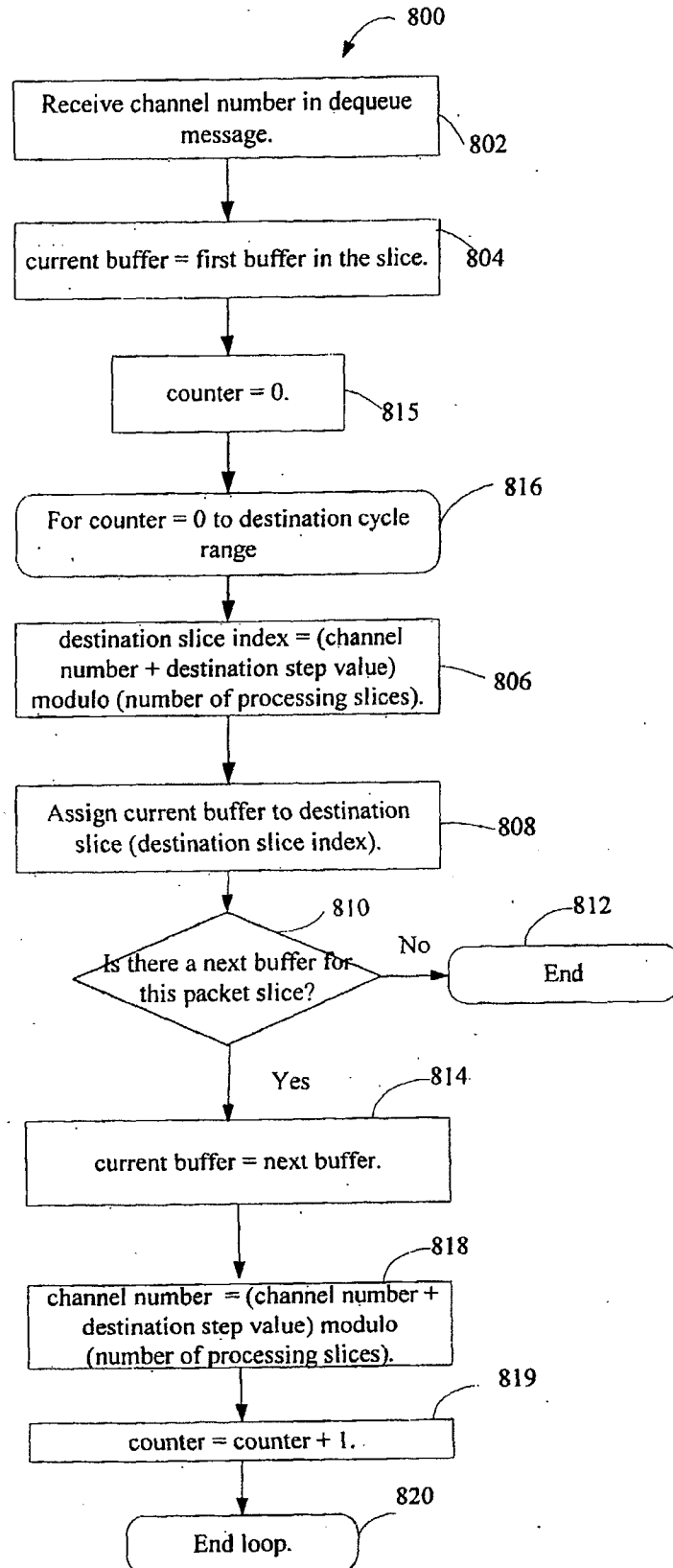


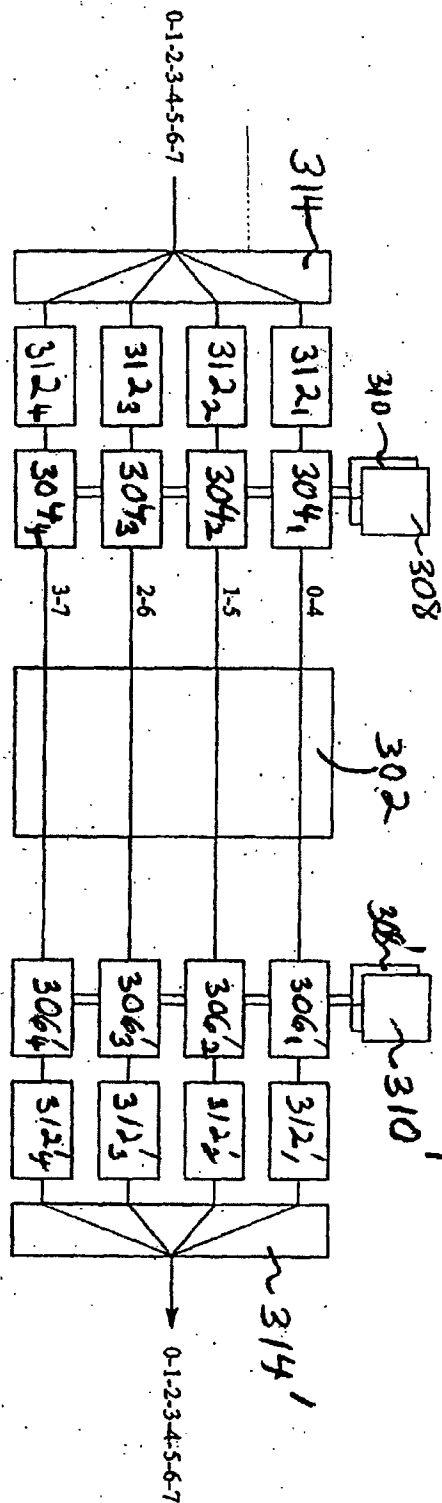


FIG. 8



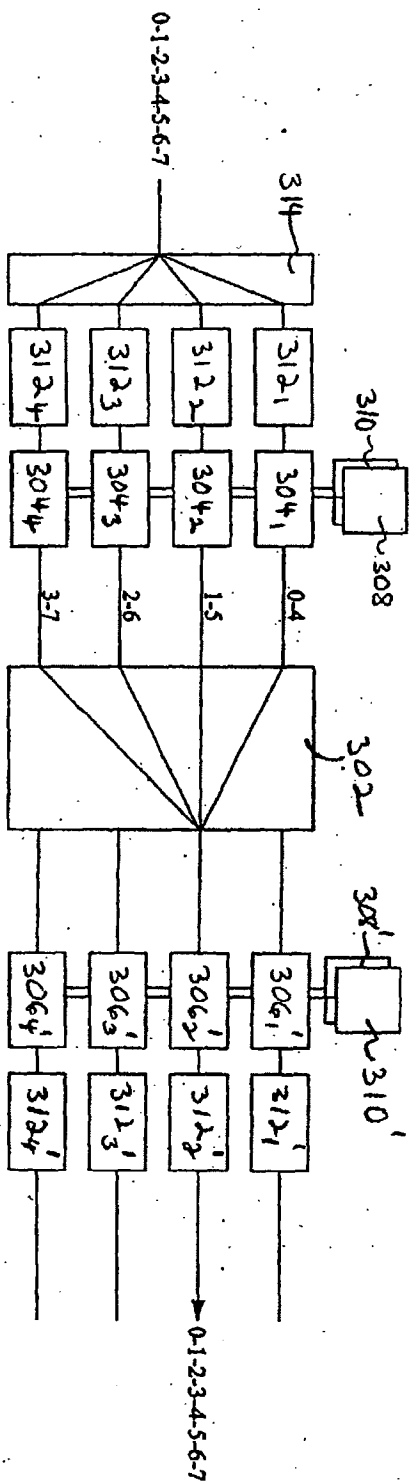
High Speed to High Speed Example

FIG. 9A



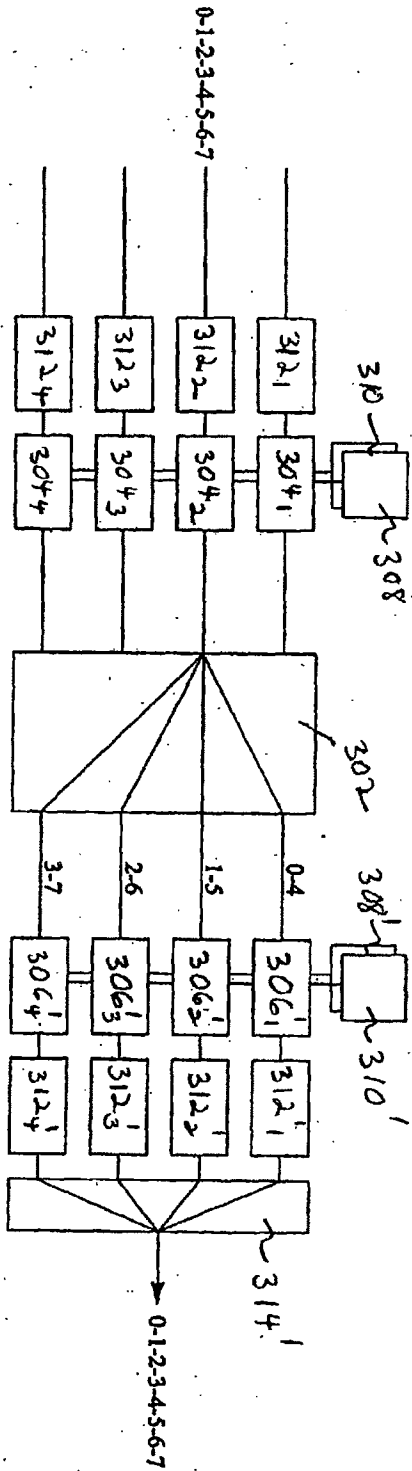
High Speed to Low Speed Example

FIG. 9B



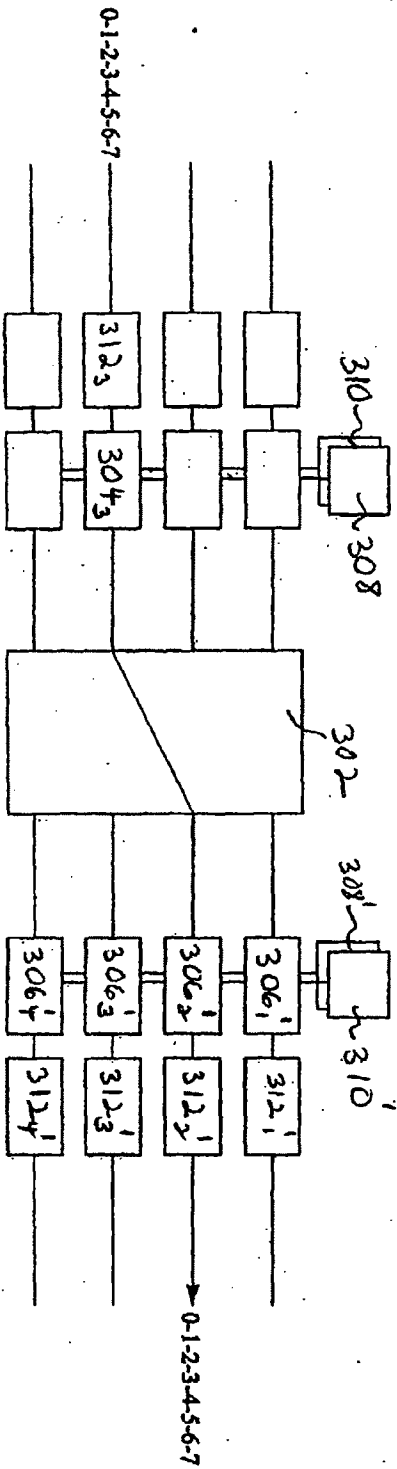
Low Speed to High Speed Example

FIG. 9C

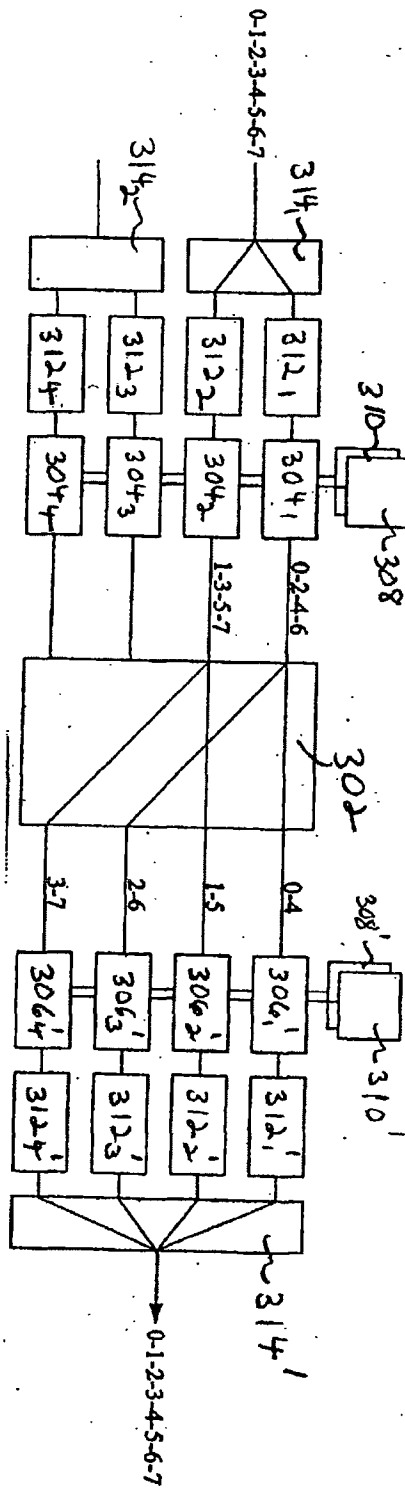


Low Speed to Low Speed Example

FIG. 9D

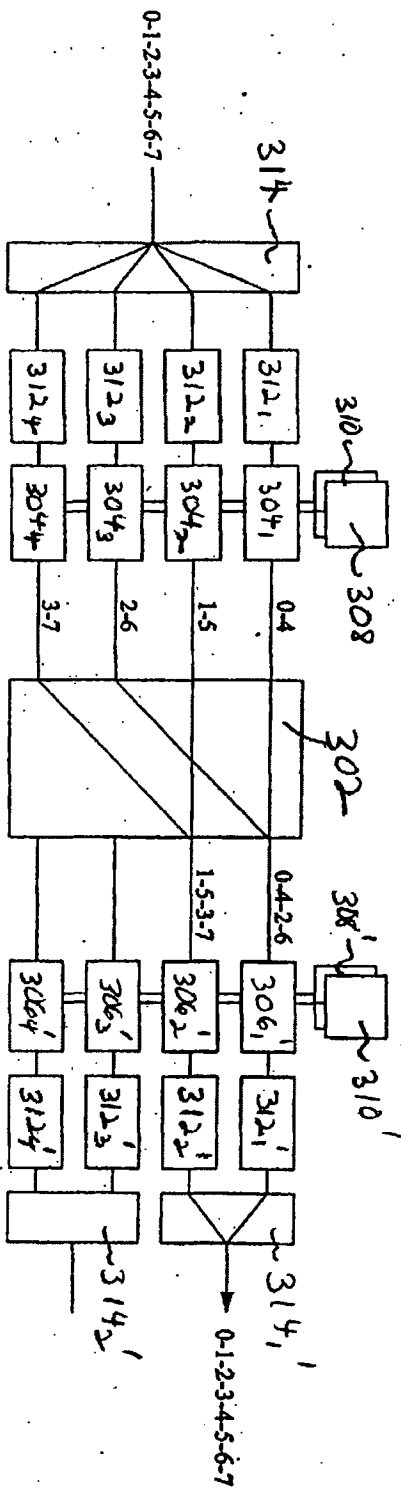


Medium Speed to High Speed Example FIG. 9E



High Speed to Medium Speed Example

FIG. 9F



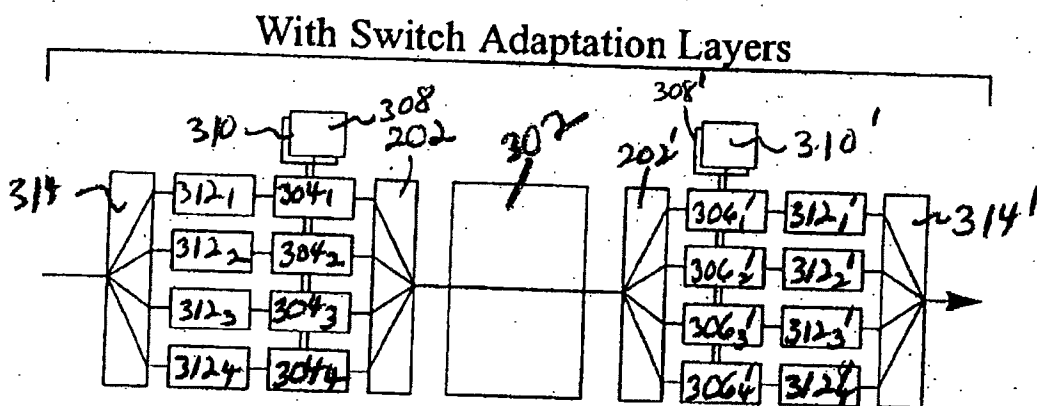


FIG. 96

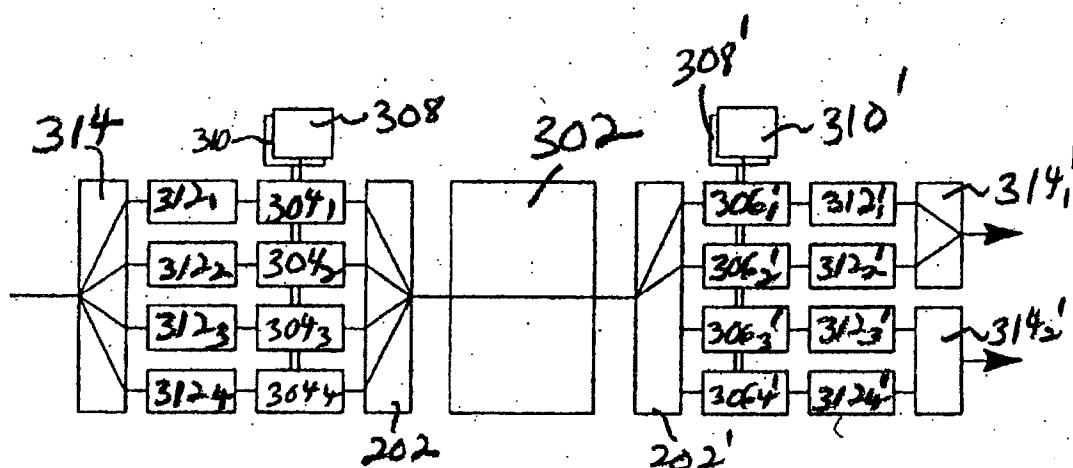
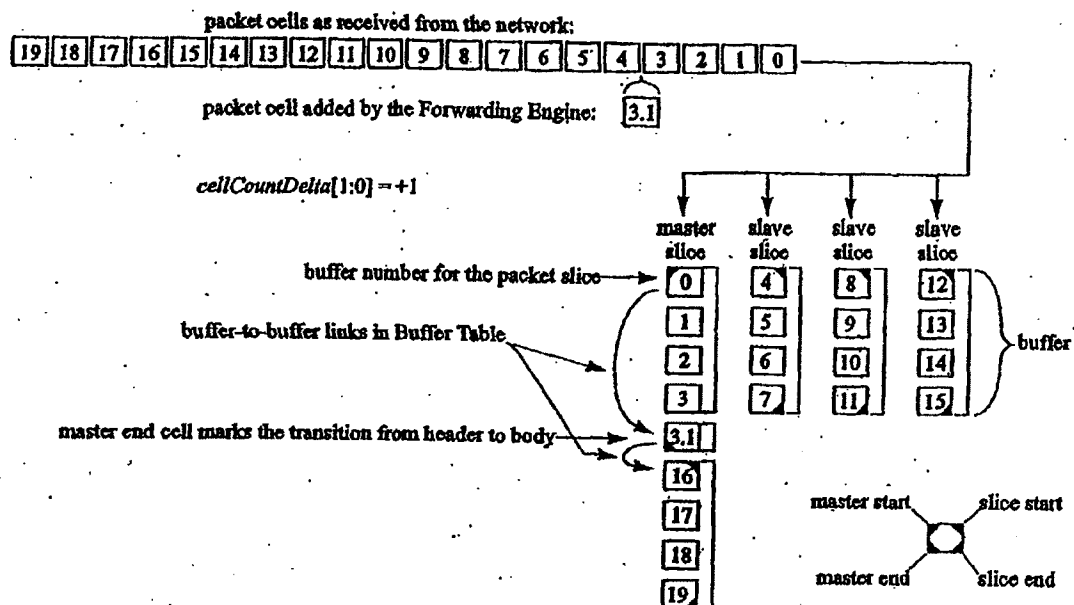


FIG. 9H

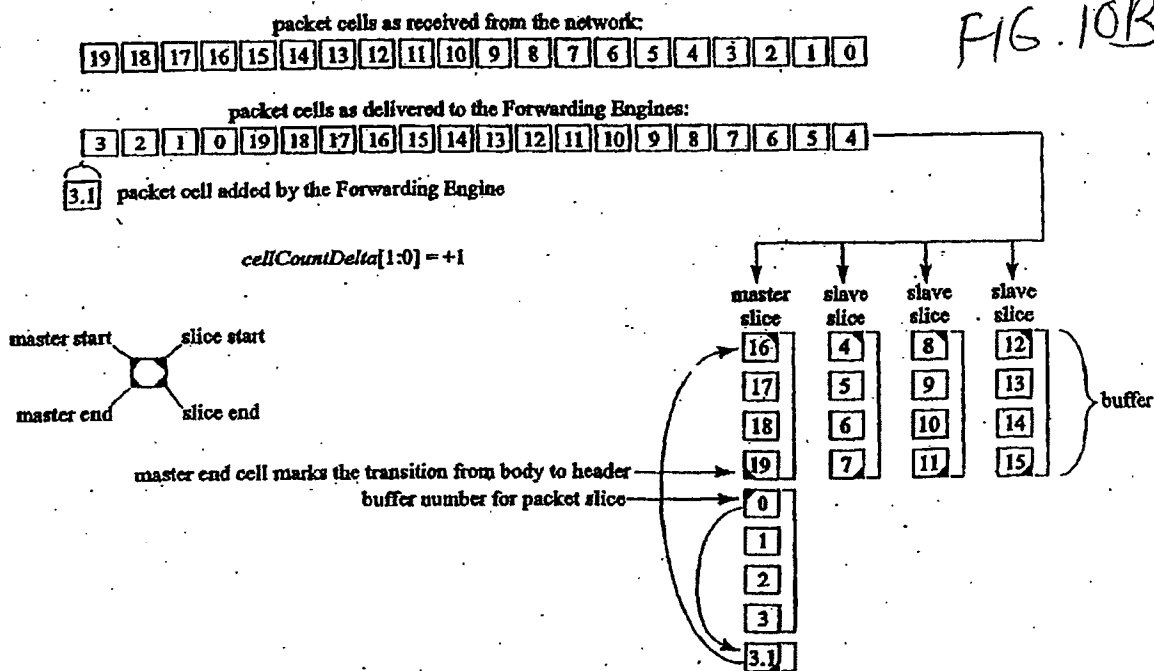
# Header Expansion on Ingress Store

FIG. 10A



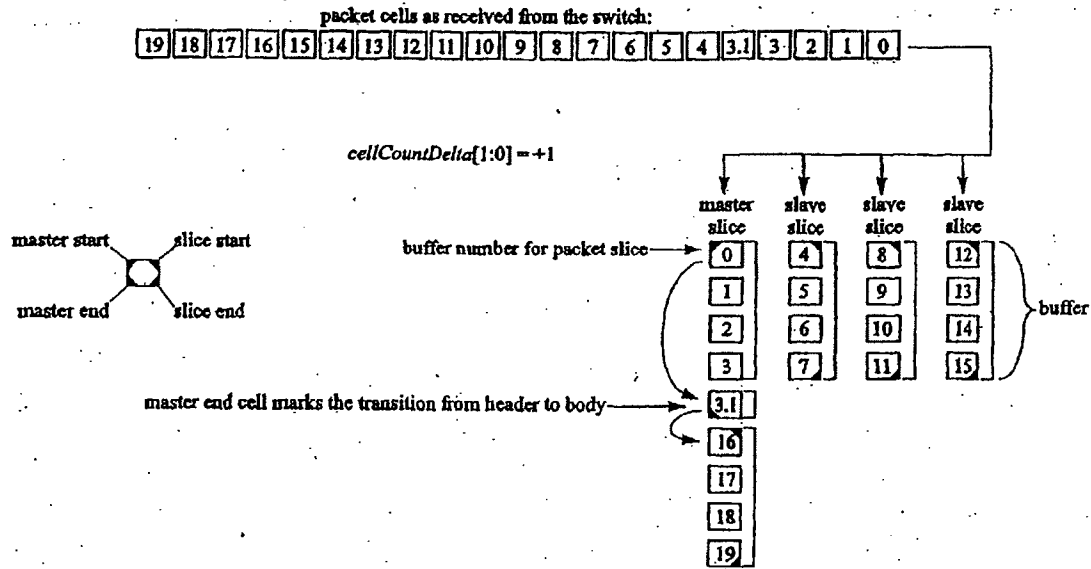
# Header Expansion on Ingress Store with Post Storage Header Delivery

FIG. 10B



Cell Sequencing on Egress Store

FIG. 10C



Cell Sequencing and Elasticity on Egress Retrieve

FIG. 10D

